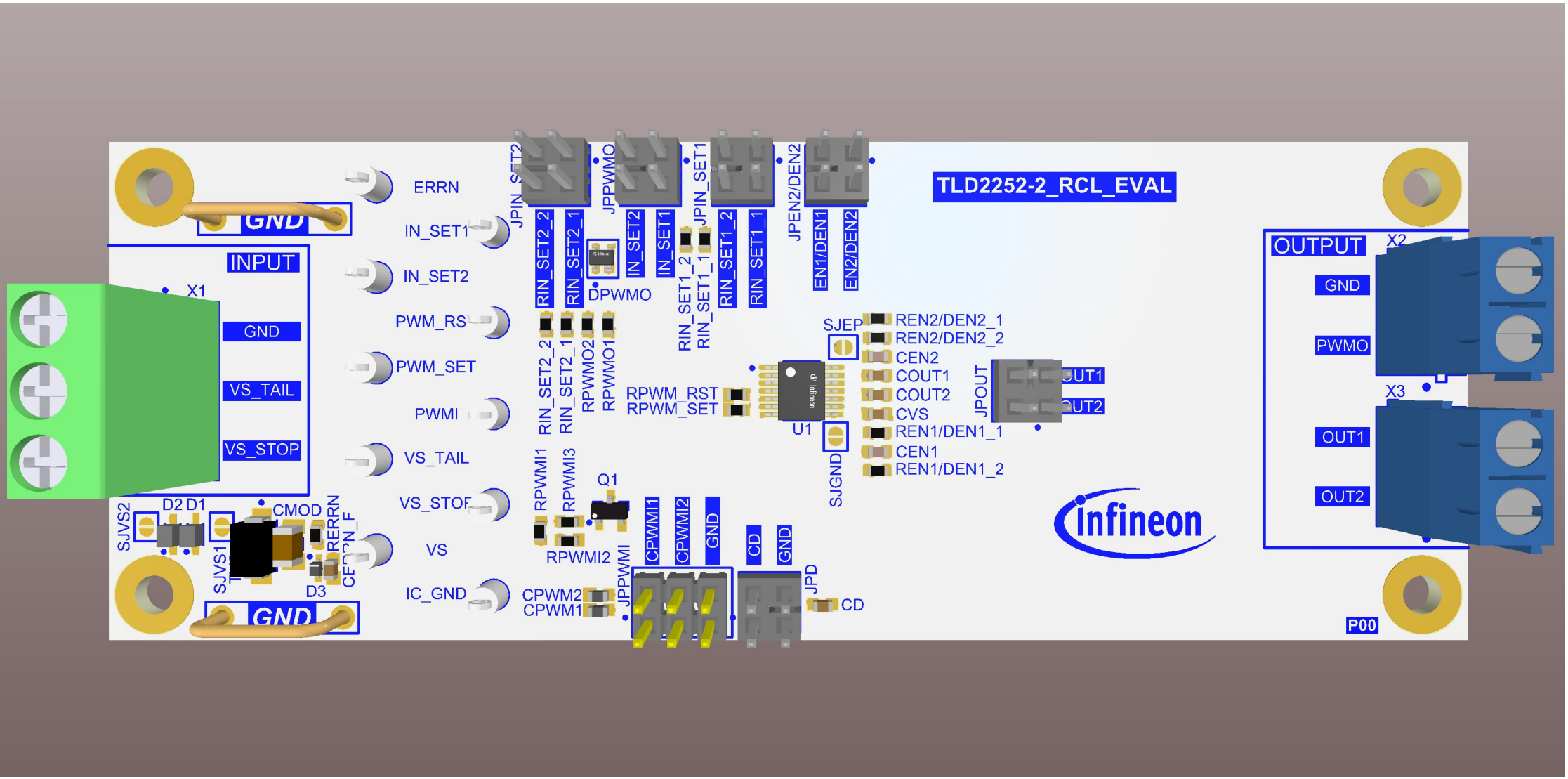
TLD2252-2_RCL_EVAL
INPUT
X1
GND
VS_TAIL
VS_STOP
GND
ERRN
IN_SET1
IN_SET2
PWM_RS
PWM_SET
PWMI
VS_TAIL
VS_STOP
VS
IC_GND
GND
SJVS2
D2
D1
SJVS1
CMOD
D3
JPIN_SET2
RIN_SET2_2
RIN_SET2_1
JPPWMO
IN_SET2
IN_SET1
DPWMO
JPIN_SET1
RIN_SET1_2
RIN_SET1_1
RIN_SET1_2
RIN_SET1_1
JPEN2/DEN2
EN1/DEN1
EN2/DEN2
RIN_SET2_2
RIN_SET2_1
RPWMO2
RPWMO1
SJEP
REN2/DEN2_1
REN2/DEN2_2
CEN2
COUT1
COUT2
CVS
REN1/DEN1_1
CEN1
REN1/DEN1_2
RPWM_RST
RPWM_SET
U1
JPOUT
SJGND
RPWMI1
RPWMI3
Q1
RPWMI2
JPPWMI
CPWMI1
CPWMI2
GND
CD
GND
JPD
CD
CPWM2
CPWM1
OUTPUT
X2
GND
PWMO
X3
OUT1
OUT2
P00